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	Filing Date		2004-03-31
	First Named Inventor	Grochowski, Edward T.	
	Art Unit	2183	
	Examiner Name	Fennema, Robert E.	
	Attorney Docket Number	P18305	

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2	476915	TW		2002-02-21	Kalafatis et al.	<input checked="" type="checkbox"/>
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7	International Preliminary Report on Patentability and Written Opinion received for PCT Application No. PCT/2005/010923, Dated Mailed 12, 2006, 7 pages.	<input type="checkbox"/>
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10	HANKINS et al., "Multiple Instruction Stream Processor", Proceedings of the 33rd International Symposium on Computer Architecture (ISCA'06), 2006, pp. 2-17/E.	<input type="checkbox"/>
11	SATO et al., "Process Management of the Operating System "Future" for On Chip Multithreaded Architecture", Proceedings of Computer System Symposium, Japan, Information Processing Society of Japan, 11 December 2003, Vol. 2001, No. 20, pp. 1-15/E.	<input type="checkbox"/>
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14	SASADA et al., "Implementation and Evaluation of a Thread Library for Multithreaded Architecture", Proceedings of the Symposium on Advanced Computing System and Infrastructure (SACIS) 2003, Information Processing Society Japan (IPSJ), 28 May 2003, Vol. 2003, No. 9, pp 1-9/E.	<input type="checkbox"/>

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